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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	Application / Conf. No.	09/746,815 / 8894
	Filing Date	December 22, 2000
	First Named Inventor	Jean-Didier Allegrucci
	Examiner Name	Brian R. Peugh
	Art Unit	2187
	Patent No.	
Mail Stop:	Attorney Docket Number	X-1761 US
Express Mail Receipt No.		
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## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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Attn: Kim Kanzaki		
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Date	July 21, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date: July 21, 2004			
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**STATEMENT UNDER 37 CFR 3.73(b)**

Applicant/Assignor/Assignee Owner: Xilinx, Inc.

Application No./Patent No.: 09/746,815 Filed/Issue Date: 12/22/2000

Entitled: Multiple Memory Aliasing For A Configurable System-On-Chip

Xilinx, Inc., a Corporation  
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or  
2. ☐ an assignee of an undivided part interest

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in the patent application/patent identified above by virtue of either:

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OR

B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: Jean-Didier Allegrucci To: Triscend Corporation  
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The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

July 21, 2004

Date



Signature

Thomas R. Lavelle

Typed or printed name

Vice President, Secretary

Title

X-1761 US